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Professor Razavi is a Fellow of IEEE, has served as an IEEE Distinguished Lecturer, and is the author of Principles of Data Conversion System Design, RF Microelectronics (translated to Chinese, Japanese, and Korean), Design of Analog CMOS Integrated Circuits (translated to Chinese, Japanese, and Korean), Design of Integrated Circuits for Optical Communications, and Fundamentals of Microelectronics (translated to Korean and Portuguese).

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Behzad Razavi received the B.Sc. degree in electrical engineering from Sharif University of Technology in 1985, and the M.Sc. and Ph.D. degrees in electrical engineering from Stanford University in 1988 and 1992, respectively. He was with AT&T Bell Laboratories and subsequently Hewlett-Packard Laboratories until 1996. He was also an Adjunct Professor at Princeton University from 1992 to 1994.

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Fundamentals of Microelectronics, 2nd Edition is designed to build a strong foundation in both design and analysis of electronic circuits this text offers conceptual understanding and mastery of the material by using modern examples to motivate and prepare readers for advanced courses and their careers. The books unique problem-solving framework enables readers to deconstruct complex problems into components that they are familiar with which builds the confidence and intuitive skills needed for success.

By helping students develop an intuitive understanding of the subject, Microelectronics teaches them to think like engineers. The second edition of Razavi's Microelectronics retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.

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communication has become almost as ubiquitous as electricity, but RF design continues to challenge engineers and researchers. In the 15 years since the first edition of this classic text, the demand for higher performance has led to an explosive growth of RF design techniques. In *RF Microelectronics, Second Edition*, Behzad Razavi systematically teaches the fundamentals as well as the state-of-the-art developments in the analysis and design of RF circuits and transceivers. Razavi has written the second edition to reflect today's RF microelectronics, covering key topics in far greater detail. At nearly three times the length of the first edition, the second edition is an indispensable tome for both students and practicing engineers. With his lucid prose, Razavi now offers a stronger tutorial focus along with hundreds of examples and problems. Teaches design as well as analysis with the aid of step-by-step design procedures and a chapter dedicated to the design of a dual-band WiFi transceiver. Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers. This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the

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band WiFi transceiver Describes new design paradigms and analysis techniques for circuits such as low-noise amplifiers, mixers, oscillators, and frequency dividers This edition's extensive coverage includes brand new chapters on mixers, passive devices, integer-N synthesizers, and fractional-N synthesizers. Razavi's teachings culminate in a new chapter that begins with WiFi's radio specifications and, step by step, designs the transceiver at the transistor level. Coverage includes Core RF principles, including noise and nonlinearity, with ties to analog design, microwave theory, and communication systems An intuitive treatment of modulation theory and wireless standards from the standpoint of the RF IC designer Transceiver architectures such as heterodyne, sliding-IF, directconversion, image-reject, and low-IF topologies. Low-noise amplifiers, including cascode common-gate and commonsource topologies, noise-cancelling schemes, and reactance-cancelling configurations Passive and active mixers, including their gain and noise analysis and new mixer topologies Voltage-controlled oscillators, phase noise mechanisms, and various VCO topologies dealing with noise-power-tuning trade-offs All-new coverage of passive devices, such as integrated inductors, MOS varactors, and transformers A chapter on the analysis and design of phase-locked loops with emphasis on low phase noise and low spur levels Two

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chapters on integer-N and fractional-N synthesizers, including the design of frequency dividers Power amplifier principles and circuit topologies along with transmitter architectures, such as polar modulation and outphasing

This market-leading textbook continues its standard of excellence and innovation built on the solid pedagogical foundation that instructors expect from Adel S. Sedra and Kenneth C. Smith. All material in the international sixth edition of Microelectronic Circuits is thoroughly updated to reflect changes in technology-CMOS technology in particular. These technological changes have shaped the book's organization and topical coverage, making it the most current resource available for teaching tomorrow's engineers how to analyze and design electronic circuits. In addition, end-of-chapter problems unique to this version of the text help preserve the integrity of instructor assignments.

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digital), on-chip interconnect modeling culminating with emerging non-silicon/ nano devices. It gives detailed description of both theoretical as well as industry standard HSPICE, Verilog, Cadence simulation based real-time modeling approach with focus on fabrication of bulk and nano-devices. Each chapter of this proposed title starts with a brief introduction of the presented topic and ends with a summary indicating the futuristic aspect including practice questions. Aimed at researchers and senior undergraduate/graduate students in electrical and electronics engineering, microelectronics, nanoelectronics and nanotechnology, this book: Provides broad and comprehensive coverage from Microelectronics to Nanoelectronics including design in analog and digital electronics. Includes HDL, and VLSI design going into the nanoelectronics arena. Discusses devices, circuit analysis, design methodology, and real-time simulation based on industry standard HSPICE tool. Explores emerging devices such as FinFETs, Tunnel FETs (TFETs) and CNTFETs including their circuit co-designing. Covers real time illustration using industry standard Verilog, Cadence and Synopsys simulations.

A transistor-level, design-intensive overview of high speed and high frequency monolithic integrated circuits for wireless and broadband

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systems from 2 GHz to 200 GHz, this comprehensive text covers high-speed, RF, mm-wave, and optical fibre circuits using nanoscale CMOS, SiGe BiCMOS, and III-V technologies. Step-by-step design methodologies, end-of chapter problems, and practical simulation and design projects are provided, making this an ideal resource for senior undergraduate and graduate courses in circuit design. With an emphasis on device-circuit topology interaction and optimization, it gives circuit designers and students alike an in-depth understanding of device structures and process limitations affecting circuit performance.

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