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Synopsis. About this title. This is a "How-To" book which explains, with hands-on examples, how to design and implement a SuperSpeed USB

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peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols. The book is based on the Cypress FX3 SuperSpeed Device and the firmware examples are written around a low-cost SuperSpeed Explorer board and a companion CPLD board which are available from [www.cypress.com/fx3book](http://www.cypress.com/fx3book).

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SuperSpeed Device Design By Example by John Hyde. English | ISBN: 1500588059 | 300 pages | EPUB | September 1, 2014 | 14 Mb. This is a "How-To" book which explains, with hands-on examples, how to design and implement a SuperSpeed USB peripheral that can interface to your hardware using a 32-bit 100MHz bus with standard or custom protocols.

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